Features:

- 768Mbps to 5Gbps duplex data links
- Compliant to 802.3z Ethernet, Fibre Channel (1x/2x/4x), Infiniband, sFPDP, XAUI, FCAV and ARINC 818
- ATEX and IECEx certified configuration available
- 850 nm VCSEL laser transmitter and PIN receiver
- Typical reach of 500 m on 50/125 and 250 m on 62.5/125 µm MMF
- Duplex LC or Dual ARINC-801 connector options
- Optional Digital Diagnostics per SFF 8472
- MIL-STD-883 compliant
- -40 to +85°C operating temperature standard, -55 to +95°C option
- Option for RoHS 6/6 compliant and lead free per Directive 2011/65/EU
- Single +3.3V power supply
- AC-Coupled Transmitter & Receiver Data

NOTE: Not recommended for new designs.

- The RJ-5G-SX-C is recommended for forward compatibility with 10G RJ transceivers

The RJ-5G-SX is ideal for harsh environment connectivity because of its low cost, availability and wide operating parameters.
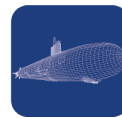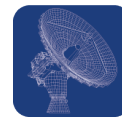
COMMERCIAL AEROSPACE   MILITARY AEROSPACE   MILITARY TACTICAL   SUBSEA NETWORKING   RADAR & SENSING   OIL & EXPLORATION

Absolute Maximum Ratings

PARAMETER	SYMBOL	MIN.	MAX.	UNIT	NOTES
Maximum Supply Voltage	V_{CC}	-0.3	4.0	V	
Storage Temperature	T_{sto}	-55	105	ºC	
Operating Temperature	T_{OP}	-40	85	ºC	-55ºC to 95ºC option available
Relative Humidity	RH	0	85	%	Based on conformal coating (1)
Hot Bar Soldering Temperature			260	ºC	10 seconds, leads only, (2)(3)
Hand Lead Soldering Temperature			260	ºC	10 seconds, leads only, (2)(3)
Conformal Coating		0.8	1.2	mil	See ruggedization notes

Notes:
1) RJ transceivers may be water washed. The process must be followed by an 80°C bake for one hour to ensure the drying of any water inside the shell.
2) For optional solder post version, solder posts are intended for mechanical retention only and do not have to comply fully to IPC J-STD-001 Class 3
3) The components should not undergo Reflow Soldering under any circumstances.
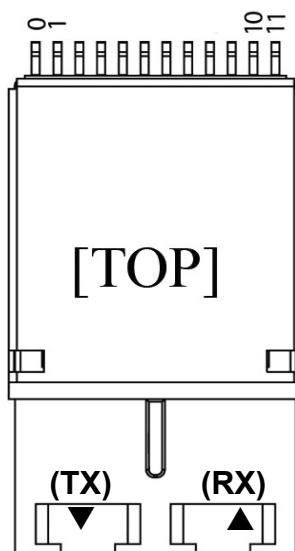
General Specifications

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	NOTES
Data Rate	BR	0.768		5	Gb/s	8b10b encoding

Electrical Specifications (T$_{OP}$ = -40 to 85°C, V$_{CC}$ = 3.14 to 3.47 Volts)

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	NOTES
Supply Voltage	V$_{CC}$	3.14	3.3	3.47	V	
Total Module Power Dissipation	P$_{DISS}$			0.83	W	
Total Supply Current (Tx + Rx)	I$_{CC}$			240	mA	
Transmitter						
Supply Current	I$_{CC}$			120	mA	
Input differential impedance	R$_{in}$	90	100	110	Ω	LVPECL
Tx Common Mode Voltage	V$_{\backslash CM}$		2.0		V	CML
Single-Ended Input Voltage Swing	V$_{DTX}$	100		1200	mV	CML
Transmit Disable Voltage	V$_D$	1.8		V$_{CC}$	V	LVTTL
Transmit Enable Voltage	V$_{EN}$	-0.3		0.8	V	LVTTL
Receiver						
Supply Current	I$_{CC}$		85	120	mA	
Single-Ended Output Voltage Swing	V$_{DRX}$	100	300	500	mV	CML
Signal Detect Assert	SD$_{norm}$	V$_{CC}$-0.5		V$_{CC}$	V	LVTTL
Signal Detect De-assert	SD$_{fault}$	0		0.4	V	LVTTL
Deterministic Jitter Contribution	RXΔDJ			0.10	UI	(1)
Total Jitter Contribution (p-p)	RXΔTJ			0.16	UI	(2)

Notes:
1) Measured running 4.25 Gb/s, K28.5 test pattern
2) Measured running 4.25 Gb/s, PRBS 2^7-1 data

Pin Configuration

PIN #	Symbol	Description	Logic/ Protocol	Notes
0	SCL	2-Wire Interface Clock	I2C	(1)
1	TD+	Transmitter DATA In +	CML	
2	V$_{EET}$	Transmitter Signal Ground	0V	
3	TD-	Transmitter DATA In -	CML	
4	V$_{CCT}$	Transmitter Power Supply	3.3V	
5	SD	Signal Detect output Satisfactory Optical Input: Logic "1" Output Fault Condition: Logic "0" Output	LVTTL	
6	T$_{DIS}$	Transmit Disable input Logic 1 = Disable Optical Output Logic 0 = Enable Optical Output Internal 10K ohm pull-down (enable)	LVTTL	
7	RD+	Receiver DATA Out +	CML	
8	V$_{CCR}$	Receiver Power Supply	3.3V	
9	RD-	Receiver DATA Out -	CML	
10	V$_{EER}$	Receiver Signal Ground	0V	
11	SDA	2-Wire Interface Data	I2C	(1)

Notes:
1) Pins 0 and 11 are the 2-wire bus pins for the digital diagnostics option
2) Pins: phosphor bronze 510 spring temper with 10 micro-inch of gold. Body is Vectra E130i
3) The RJ-5G-SX is not recommended for new designs.
    a. The RJ-5G-SX-C should be utilized in new designs to ensure forward compatibility with COTSWORKS' 10G RJ transceiver pinout configuration

Optical Characteristics (T_{OP} = -40 to 85°C, V_{CC} = 3.14 to 3.47 Volts)

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	NOTES
Transmitter						
Optical Output Power	P_{OUT}	-5		-1	dBm	(1)(4)
Optical Output Power	P_{OUT}	-7		-1	dBm	(1)(3)(4)
Optical Wavelength	λ	840	850	860	nm	(2)
RMS Spectral Width	σ			.85	nm	(2)
Extinction Ratio	ER	6			dB	(2)(3)
Optical Rise Time	t_r			90	ps	Unfiltered 20% -
Optical Fall Time	t_f			125	ps	80%, (1)(2)
Relative Intensity Noise	RIN			-117	dB/Hz	
Deterministic Jitter Contribution	TXΔDJ			0.12	UI	(6)
Total Jitter Contribution (p-p)	TXΔTJ			0.40	UI	(2)
Receiver						
Sensitivity: 5 Gb/s	RX_{SENS5}			-14	dBm	
Sensitivity: 4.25 Gb/s	RX_{SENS4}			-16	dBm	
Sensitivity: 3.00 Gb/s	RX_{SENS3}			-16	dBm	
Sensitivity: 2.125 Gb/s	RX_{SENS2}			-18	dBm	(5)
Sensitivity: 1.25 Gb/s	RX_{SENS1}			-21	dBm	
Sensitivity: 768 Mb/s	RX_{SENS0}			-18	dBm	
Overload	RX_{MAX}			0	dBm	
Optical Center Wavelength	λ_c	840		860	nm	
Return Loss	RL	12			dB	
Signal Detect Assert: 5.00 Gb/s	P_{A5}			-14	dBm	
Signal Detect Assert: 4.25 Gb/s	P_{A4}			-15	dBm	
Signal Detect Assert: 3.00 Gb/s	P_{A3}			-16	dBm	
Signal Detect Assert: 2.125 Gb/s	P_{A2}			-17	dBm	
Signal Detect Assert: 1.25 Gb/s	P_{A1}			-20	dBm	
Signal Detect Assert: 768 Mb/s	P_{A0}			-19	dBm	
Signal Detect De-Assert	P_D	-29			dBm	All data rates
Signal Detect Hysteresis	$P_A - P_D$	1		5	dB	

Notes:
1) Measured at the end of a 2m to 5m 62.5μm multi-mode fiber patch cord
2) Measured running 4.25 Gb/s, PRBS 2^7-1 data
3) Applicable between the extended temperature ranges of -55°C to -40°C and 85°C to 95°C
4) Class 1 Laser Safety per FDA/CDRH and IEC-825-1 regulations
5) Measured using PRBS 2^7-1 pattern
6) Measured running 4.25 Gb/s, K28.5 test pattern

Digital Diagnostics Information

The COTSWORKS RJ module is available with optional signal pins for a 2-wire bus required to access digital diagnostics compliant to SFF 8472 multi-source agreement. The transceiver pinout (including those pins required for 2-wire communication to access the digital diagnostics) is shown on page 2 of this document.

Address A0h Data Fields

A0h Address (dec)	# Bytes	Name	Description	Value (hex)
Base ID Fields				
00	1	Identifier	Type of transceiver	02
01	1	Ext. Identifier	Extended identifier of type of transceiver	04
02	1	Connector	Code for connector type	07

Address	Size	Name	Description	Value
03	8	Transceiver	Code for electronic or optical compatibility	08
04				00
05				00
06				01
07				40
08				40
09				0C
10				15
11	1	Encoding	Code for high speed serial encoding algorithm	01
12	1	BR, Nominal	Nominal signaling rate, units of 100 MBd	32
13	1	Rate Identifier	Type of rate select functionality	00
14	1	Length (SMF, km)	Link length supported for single mode fiber, units of km	00
15	1	Length (SMF)	Link length supported for single mode fiber, units of 100 m	00
16	1	Length (50um)	Link length supported for 50 um OM2 fiber, units of 10 m	37
17	1	Length (62.5um)	Link length supported for 62.5 um OM1 fiber, units of 10 m	1C
18	1	Length (OM4 or copper cable)	Link length supported for 50um OM4 fiber, units of 10m. Alternatively copper or direct attach cable, units of m	64
19	1	Length (OM3)	Link length supported for 50 um OM3 fiber, units of 10 m	37
20	16	Vendor Name	SFP vendor name (ASCII)	43
21				4F
22				54
23				53
24				57
25				4F
26				52
27				4B
28				53
29				20
30				20
31				20
32				20
33				20
34				20
35				20
36	1	Transceiver	Code for electronic or optical compatibility	00
37	3	Vendor OUI	SFP vendor IEEE company ID	00
38				00
39				00
40	16	Vendor PN	Part number provided by SFP vendor (ASCII)	52
41				4A
42				XX
43				47
44				53
45				58
46				XX
47				XX

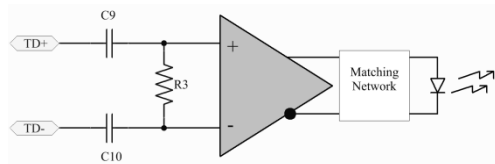|				
48				XX
49				XX
50				XX
51				XX
52				XX
53				XX
54				XX
55				XX
56	4	Vendor rev	Revision level for part number provided by vendor (ASCII)	30
57				30
58				30
59				30
60	2	Wavelength	Laser wavelength	03
61				52
62	1	Unallocated		00
63	1	CC_BASE	Check code for Base ID Fields (addresses 0 to 62)	XX
Extended ID Fields				
64	2	Options	Indicates which optional transceiver signals are implemented	10
65				14
66	1	BR, max	Upper bit rate margin, units of %	00
67	1	BR, min	Lower bit rate margin, units of %	00
68	16	Vendor SN	Serial number provided by vendor (ASCII)	XX
69				XX
70				XX
71				XX
72				XX
73				XX
74				XX
75				XX
76				XX
77				XX
78				XX
79				XX
80				XX
81				XX
82				XX
83				XX
84	8	Date code	Vendor's manufacturing date code	XX
85				XX
86				XX
87				XX
88				XX
89				XX
90				20
91				20
92	1	Diagnostic Monitoring Type	Indicates which type of diagnostic monitoring is implemented (if any) in the transceiver	68
93	1	Enhanced Options	Indicates which optional enhanced features are implemented (if any) in the transceiver	70
94	1	SFF-8472 Compliance	Indicates which revision of SFF-8472 the transceiver complies with	08
95	1	CC_EXT	Check code for the Extended ID Fields (addresses 64 to 94)	XX

Application Schematics

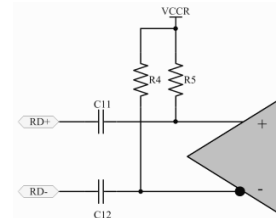**HOST BOARD APPLICATION SCHEMATIC**
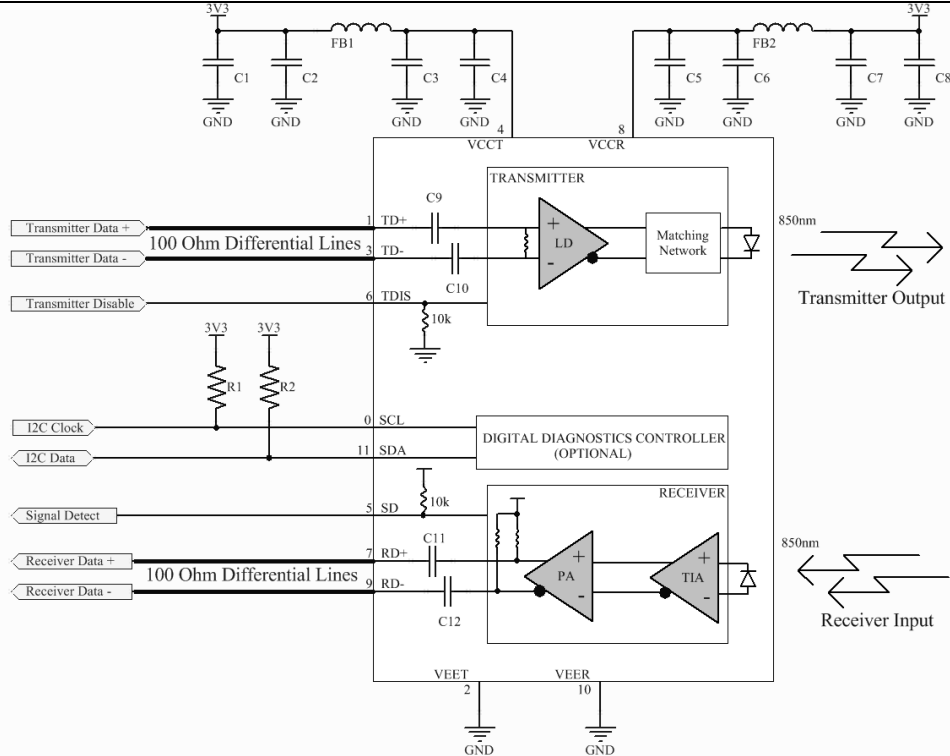
Notes:

- Recommend host routes separate supply voltages and filtering for RJ-module transmitter and receiver as shown in the schematic above:
    - FB1/FB2 ferrite bead for power supply noise suppresion; Murata BLM18KG601SN1, 0603, 600Ω @ 100MHz, 1300mA .
    - C1/C4/C5/C8 bulk capacitance; Murata GRM21BR61C106KE15L, 0805, 10µF, 16V.
    - C2/C3/C6/C7 de-coupling capacitors; Murata GRM155R71C104KA88D, 0402, 0.1µF, 16V.
- R1/R2 2-wire bus pull-up resistors required on host for implementing optional digital diagnostics; 4.7kΩ to 10kΩ.
- Screw or solder posts are not internally connected to signal ground. Recommend screw or solder posts be connected to chassis ground if available, otherwise they should be tied to local signal ground.
- For host with LVPECL electrical interface contact COTSWORKS' applications engineering.

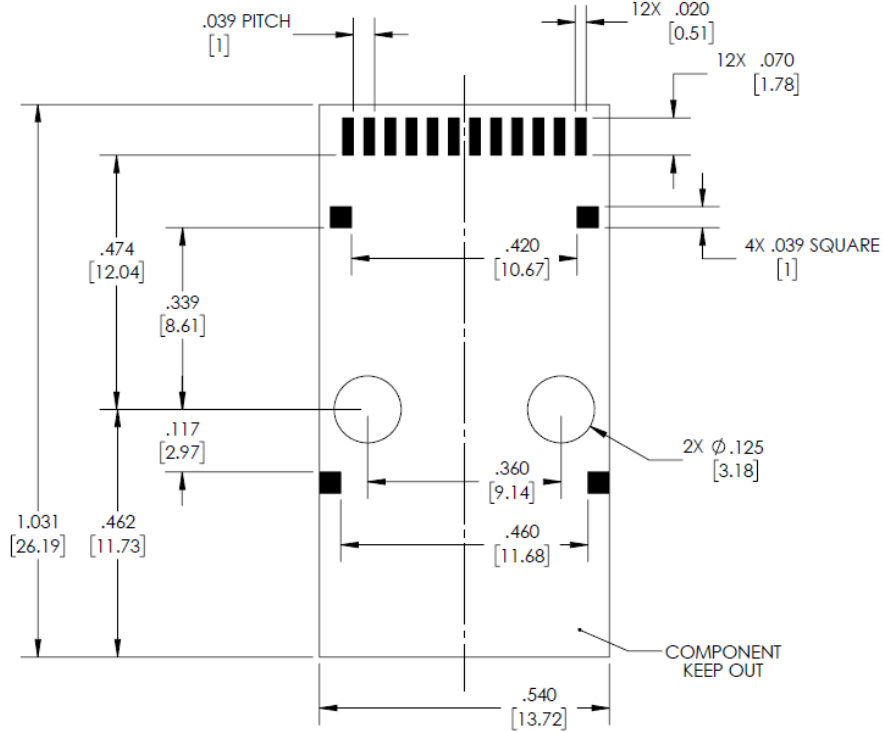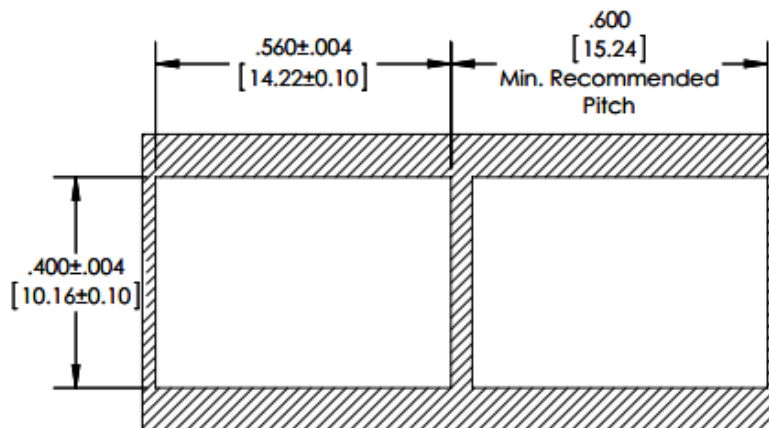| TRANSMITTER EQUIVALENT INPUT CIRCUIT	RECEIVER EQUIVALENT OUTPUT CIRCUIT
- C9/C10 0.1µF internal input data coupling capacitors. - R3 internal 100Ω input differential termination. - Transmitter electrical input is CML compatible.	- C11/C12 are 0.1µF output data coupling capacitors. - R4/R5 are 50Ω pull-up resistors to Vcc. - Receiver electrical output is CML compatible.

PCB Design Guidelines
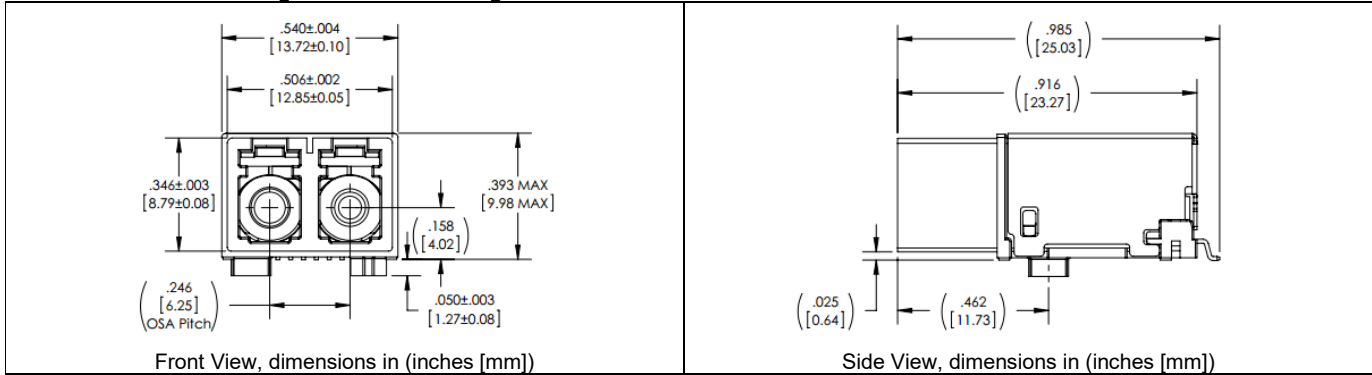

.039 PITCH
[1]

12X .020
[0.51]

12X .070
[1.78]

.474
[12.04]

.420
[10.67]

4X .039 SQUARE
[1]

.339
[8.61]

.117
[2.97]

.360
[9.14]

2X Ø.125
[3.18]

1.031
[26.19]

.462
[11.73]

.460
[11.68]

COMPONENT
KEEP OUT

.540
[13.72]

Notes:
1) Pads 0 and 11 are the 2-wire bus signal pin pads for the digital diagnostics option

Panel Cutout

.560±.004
[14.22±0.10]

.600
[15.24]
Min. Recommended
Pitch

.400±.004
[10.16±0.10]

LC Connector [Screw Post] Mechanical Dimensions

Front View, dimensions in (inches [mm])

Side View, dimensions in (inches [mm])

LC Connector [Solder Post] Mechanical Dimensions

Front View, dimensions in (inches [mm])

Side View, dimensions in (inches [mm])

ARINC-801 Connector [Screw Post] Mechanical Dimensions

Front View, dimensions in (inches [mm])

Side View, dimensions in (inches [mm])
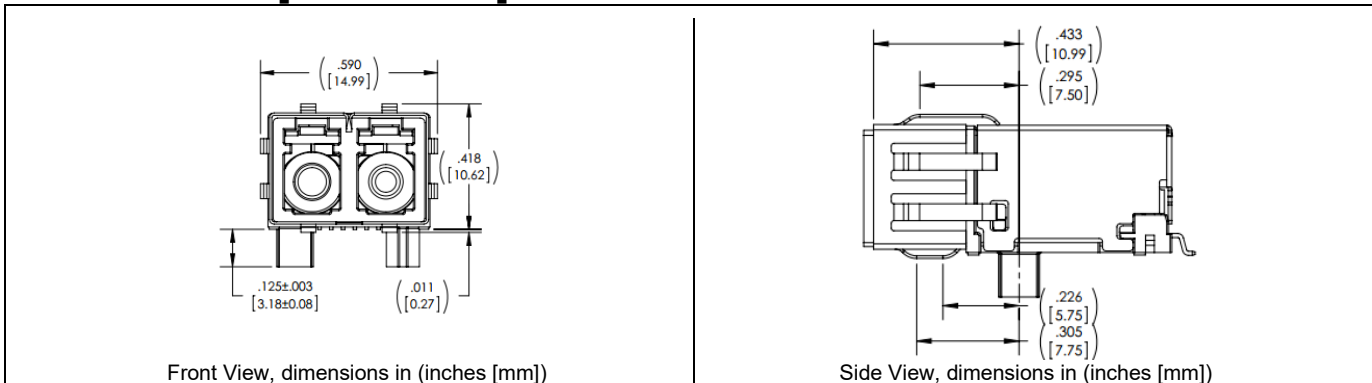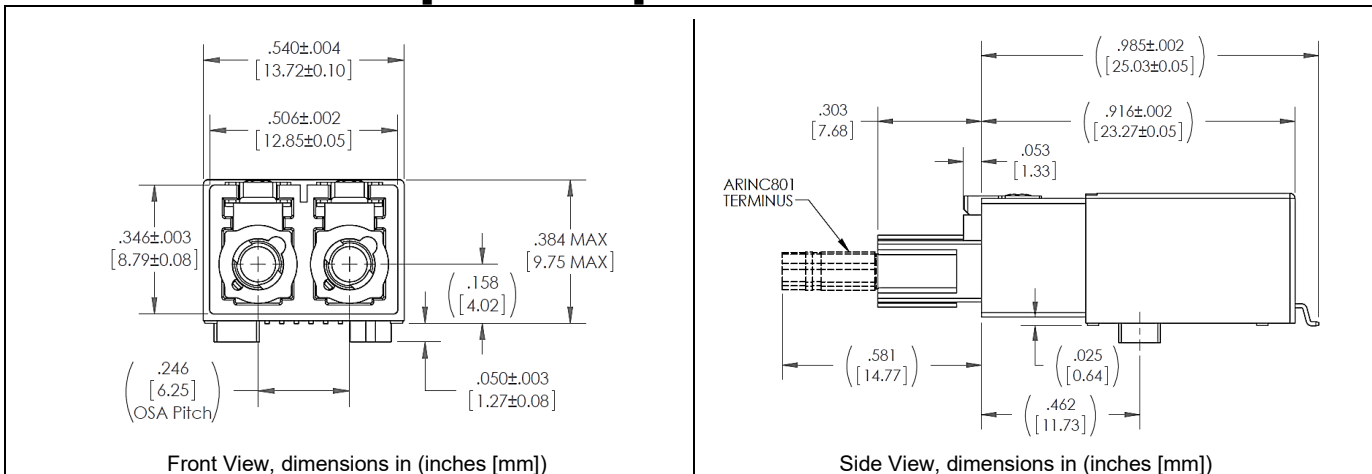
Mounting Hardware Guidelines

Notes:
1. An example illustrating a hardware combination to secure RJ-5G-SX to host PCB
       For further mounting hardware options and support contact COTSWORKS Application Engineering
2. When installing the RJ module:
       i. install the washers and partially tighten the screws,
       ii. solder the leads,
       iii. tighten the screws to 12 in-oz
3. The pins are phosphor bronze 510 spring temper with 10 micro-inches of gold.

Ruggedization Notes

- Parylene C coating can be used for conformal coating with a 1.0 mil ± 0.2 mil thickness through a deposition process
    o It has a 5600 VPM rating, withstands high temperatures, extremely resistant to oil/dirt, and object impact
- This part is also available in a pigtail fiber optic version. Contact COTSWORKS for available fiber and termini options.
- Transceiver case is nickel-plated

Reference Information

1) IEEE Standard 802.3, 2002 Edition
2) Directive 2011/65/EU of the European Parliament and of the Council, "on the restriction of the use of certain hazardous substances in electrical and electronic equipment." June 8th, 2011

Regulatory Compliance

- COTSWORKS transceivers are Class 1 Laser Products and designed to comply with US FDA regulations.
- These products are designed to comply with Class 1 eye safety requirements of EN (IEC) 60825 and the electrical safety requirements of EN (IEC) 60950.
- This part has an option for compliance with Directive 2011/65/EU covering restriction on certain hazardous substances (RoHS)
- ATEX / Ex Protection Provided: op is, [Ex op is T4 Ga] IIC
    o Contact COTSWORKS support for a product compliance matrix

Warnings:

Handling Precautions: This device is susceptible to damage as a result of electrostatic discharge (ESD). A static free environment is highly recommended.
Laser Safety: Radiation emitted by laser devices can be dangerous to human eyes. Avoid eye exposure to direct or indirect radiation.

Ordering Information

RJ-5G-SX	-x-	DPLX-xx-	x	-x-	x	-x-	x	-x
RJ Form Factor	Pins & Diagnostics	Receptacle Type	Ruggedized Coating	Operating Temp (ºC)	EMI Shield?	RoHS Level	Mounting	ATEX & IECEx Cert.
5 Gbps Max Data Rate	(): 1x10 No DDMI	LC: LC Receptacle	(): Non-coated	A: -40 to 85	(): No Shield	(): Lvl 5	(): Imperial	T: Certified
				M: -40 to 95			U: Metric	
(MMF)	D: 1x12 DDMI	LX: ARINC-801 Receptacle	R: Parylene	Z: -55 to 95	E: Shield	6 Lvl 6	P: Solder Posts	(): Not Certified

Example part number: RJ-5G-SX-D-DPLX-LX-R-A
[5G RJ Form Factor Transceiver, 850nm, digital diagnostics,
Duplex ARINC-801 connectors, Parylene-coated, industrial operating temp range, Imperial Screw Posts]

NOTE: The RJ-5G-SX is not recommended for new designs.
The RJ-5G-SX-C should be utilized in new designs to ensure forward compatibility with the
COTSWORKS 10G RJ transceiver pinout configuration.
Please refer to the COTSWORKS RJ-5G-SX-C datasheet for more information.

Contact COTSWORKS for mechanical dimensional information and other configuration options.